



# M.2 to USB Adapter

E-Key M.2 Adapter
Datasheet



## M.2 to USB Adapter

An E-key M.2 to USB-C adapter, allowing users to test M.2 module interfaces





Breaking out M.2 low-speed interfaces is easy with the Tier One M.2 to USB Adapter. The adapter is ideal for hosting plug-in M.2 Type E communication modules where low power noise is crucial to optimizing receive data performance in wireless receivers.

Supported Interfaces: USB 2.0, I2C, and CMOS (1.8V) UART communication channels

M.2 Size Support: 2230, 3030, and 3042

M.2 Key: Type E

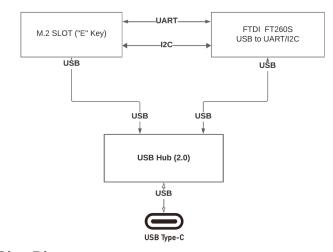
#### **Features**

- Made in the USA from globally sourced components.
- Ultra-low noise slot power to maximize receive sensitivity of modules
- USB Type C interface (USB 2.0 support)
- M.2 Module-controlled LED
- Optional trusted platform module (Microchip ATEC)

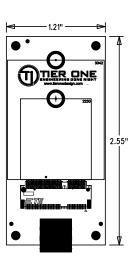
| Operating Conditions                       |                        |  |  |  |
|--|------------------------|--|--|--|
| Ambient Temperature                        | -40°C - 85°C           |  |  |  |
| V <sub>USB</sub> Current (Socket Unloaded) | 20 mA                  |  |  |  |
| Socket 3.3V Range (200mA)*                 | 3.24V - 3.46V (+/- 2%) |  |  |  |
| PERST# Asserted                            | 2.94V                  |  |  |  |
| 1.8V Range                                 | 1.76V -1.84V (+/- 2%)  |  |  |  |

\*NOTE: Socket power can support up to 1A in peak bursts. Higher sustained currents are possible with a reduced MAX ambient temperature

#### **Block Diagram**



#### Size Diagram





# **Pin Definitions and Supported Pins**

All pinouts in this section are written from the adapter point of view when referencing signal direction.

|           | Abbreviation | Definition              |  |
|-----------|--------------|-------------------------|--|
| Pin Types | U            | USB Signaling           |  |
|           | I            | Input from module       |  |
|           | 1/0          | Bidirectional signaling |  |
|           | 0            | Output to module        |  |

| SKT Pin#  | M.2 Name | Туре | Domain | Module Type |
|-----------|----------|------|--------|-------------|
| 3         | USB_D-   | U    |        | U           |
| 5         | USB_D+   | U    |        | U           |
| 6         | LED1#    | I    | 3.3V   | 0           |
| 22        | UART_RXD | I    | 1.8V   | 0           |
| 32        | UART_TXD | 0    | 1.8V   | 1           |
| 34        | UART_CTS | I    | 1.8V   | 0           |
| 36        | UART_RTS | 0    | 1.8V   | I           |
| <b>52</b> | PERST#   | 0    | 3.3V   | I           |
| 58        | I2C_Data | 1/0  | 3.3V   | I           |
| 60        | I2C_CLK  | 0    | 3.3V   | I           |
| 62        | ALERT#   | I    | 3.3V   | I           |

|       | Power Domain |                   | Description                             | Min  | Max | Unit |
|-------|--------------|-------------------|---|------|-----|------|
| Level | 1.8V         | V <sub>IL</sub>   | Low-level Input                         |      | 0.8 | V    |
|       |              | $V_{\text{IH}}$   | High-level Input                        | 1.17 |     | V    |
|       |              | V <sub>o</sub>    | Output Voltage (I <sub>OUT</sub> < 8mA) | 0    | 1.8 | V    |
|       |              | I <sub>OH/L</sub> | Output/Input Current                    |      | 8   | mA   |
|       | 3.3V         | V <sub>IL</sub>   | Low-level Input                         |      |     | V    |
|       |              | V <sub>IH</sub>   | High-level Input                        | 2.0  |     | V    |
|       |              | V <sub>o</sub>    | Output Voltage (I <sub>OUT</sub> < 8mA) | 0    | 3.3 | V    |
|       |              | I <sub>OH/L</sub> | Output/Input Current                    |      | 4   | mA   |



## **Reference Documents**

The following documents are external reference documents and should be consulted when applicable:

- PCI Sig M.2 Electromechanical Specification Revision 5.1, Version 1.0 2023
- USB Specifications (<u>www.usb.org</u>)
- The I2C Specification, Version 2.1 January 2000, Philips Semiconductor (now NXP: <a href="https://www.nxp.com">www.nxp.com</a>)
- FTDI FT260S HID class USB to UART/I2C Master (www.ftdichip.com)

# TIER DINEERING DONE RIGHT

Tier One, Inc. or other entities may possess intellectual property rights in the products, names, logos, and designs featured in this document. Any duplication, reproduction, or alteration of this document, or any portion of it, is authorized exclusively with the direct written consent of Tier One, Inc. Sharing of this document with external parties is allowed solely for documents that are unequivocally intended for public dissemination. The information provided in this document is offered "as is," and Tier One, Inc. bears no responsibility for its application. No guarantees, either explicit or implied, are made, including, but not limited to, guarantees regarding accuracy, precision, dependability, or suitability for a specific purpose of the provided information.